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Science is continually confronted by new and difficult social and ethical problems. Some of these problems have arisen from the transformation of the academic science of the prewar period into the industrialized science of the present. Traditional theories of science are now widely recognized as obsolete. In *Scientific Knowledge and Its Social Problems* (originally published in 1971), Jerome R. Ravetz analyzes the work of science as the creation and investigation of problems. He demonstrates the role of choice and value judgment, and the inevitability of error, in scientific research. Ravetz's new introductory essay is a masterful statement of how our understanding of science has evolved over the last two decades. Top-notch advice on adopting DSL DSL is exploding, with fifteen million fast Internet connections in 2001 and probably forty million more by 2003. The editors of DSL Prime, the weekly "industry bible," brief you on exactly what you need to know to put the technology to use. The book moves rapidly from the basics of the equipment through security, applications, and network management. With the authority of the industry's key periodical, Bourne and Burstein provide candid evaluations of the providers and manufacturers and offer technical managers a no-nonsense guide to make sound strategic decisions on DSL, reduce costs, and ensure a system that will run reliably and smoothly. Wiley Tech Briefs Focused on the needs of the corporate IT and business manager, the Tech Briefs series provides in-depth information on a new or emerging technology, solutions, and vendor offerings available in the marketplace. With their accessible approach, these books will help you get quickly up-to-speed on a topic so that you can effectively compete, grow, and better serve your customers. This book deals with modeling and implementation of high performance, current-steering D/A-converters for digital transceivers in nanometer CMOS technology. In the first part, the fundamental performance limitations of current-steering DACs are discussed. Based on simplified models, closed-form expressions for a number of basic non-ideal effects are derived and tested. With the knowledge of basic performance limits, the converter and system architecture can be optimized in an early design phase, trading off circuit complexity, silicon area and power dissipation for static and dynamic performance. The second part describes four different current-steering DAC designs in standard 130 nm CMOS. The converters have a resolution in the range of 12-14 bits for an analog bandwidth between 2.2 MHz and 50 MHz and sampling rates from 100 MHz to 350 MHz. Dynamic-Element-Matching (DEM) and advanced dynamic current calibration techniques are employed to minimize the required silicon area. The *Architecture of Computer Hardware, Systems Software and Networking* is designed help students majoring in information technology (IT) and information systems (IS) understand the structure and operation of computers and computer-based devices. Requiring only basic computer skills, this accessible textbook introduces the basic principles of system architecture and explores current technological practices and trends using clear, easy-to-understand language. Throughout the text, numerous relatable examples, subject-specific illustrations, and in-depth case studies reinforce key learning points and show students how important concepts are applied in the real world. This fully-updated sixth edition features a wealth of new and revised content that reflects today's technological landscape. Organized into five parts, the book first explains the role of the computer in information systems and provides an overview of its components. Subsequent sections discuss the representation of data in the computer, hardware architecture and operational concepts, the basics of computer networking, system software and operating systems, and various interconnected systems and components. Students are introduced to the material using ideas already familiar to them, allowing them to gradually build upon what they have learned without being overwhelmed and develop a deeper knowledge of computer architecture. Over the last ten years, the ARM architecture has become one of the most pervasive architectures in the world, with more than 2 billion ARM-based processors embedded in products ranging from cell phones to automotive braking systems. A world-wide community of ARM developers in semiconductor and product design companies includes software developers, system designers and hardware engineers. To date no book has directly addressed their need to develop the system and software for an ARM-based system. This text fills that gap. This book provides a comprehensive description of the operation of the ARM core from a developer's perspective with a clear emphasis on software. It demonstrates not only how to write efficient ARM software in C and assembly but also how to optimize code. Example code throughout the book can be integrated into commercial products or used as templates to enable quick creation of productive software. The book covers both the ARM and Thumb instruction sets, covers Intel's XScale Processors, outlines distinctions among the versions of the ARM architecture, demonstrates how to implement DSP algorithms, explains exception and interrupt handling, describes the cache technologies that surround the ARM cores as well as the most efficient memory management techniques. A final chapter looks forward to the future of the ARM architecture considering ARMv6, the latest change to the instruction set, which has been designed to improve the DSP and media processing capabilities of the architecture. * No other book describes the ARM core from a system and software perspective. * Author team combines extensive ARM software engineering experience with an in-depth knowledge of ARM developer needs. * Practical, executable code is fully explained in the book and available on the publisher's Website. * Includes a simple embedded operating system. *Massie Block*: Stands at the threshold of the most ah-mazing secret room at OCD. But to prove the Pretty Committee is worthy of the honor, the girls need dates to Skye Hamilton's "famous couples" costume party. On top of all that, Massie has to convince her old crush, Chris Abeley, to be Skye's date . . . or else they can kiss the room-and their popularity-goodbye. How will she ever get her girls suitable dates and melt Chris's heart? She'd need to be a mind reader! Luckily for the PC, alpha status just happens to come with ESP . . . not the actual superpower, but a secret weapon that shows them exactly how boy brains work. *Kristen Gregory*: Loves supersensitive guys, and thanks to ESP, has her eye on a boy who loves The Notebook as much as she does. So why on earth is she going to Skye's party as the Bride of Chucky? *Alicia Rivera*: Diss-missed her crush, Josh Hotz, for having a bizarrely clean room. Then ESP tells her Josh's sister is actually the neat freak, and suddenly hawt Hotz looks hawter than ever. *Dylan Marvil*: When ESP clues her in to the fact that guys-plural- like a girl who's not afraid to eat, it's all mozzarella sticks for this former Jenny Craig groupie. Will gaining six pounds make her regret trusting her sixth sense? *Claire Lyons*: As the only Pretty Committee girl with a real boyfriend, the date-mission should be a breeze. But when her newfound Claire-voyance tells her Cam's hiding a huh-yuge secret from her, his gifts of gummies leave a stale taste in her mouth. ESP? Extra Sucky Problem is more like it. *The Clique*...The only thing harder than getting in is staying in. This IBM Redbooks publication focuses on the differences introduced in AIX 5L Version 5.3 when compared to AIX 5L Version 5.2. It is intended to help system administrators, developers, and

users understand these enhancements and evaluate potential benefits in their own environments. AIX 5L Version 5.3 introduces many new features, including NFS Version 4 and Advanced Accounting, and exploits the advanced capabilities of POWER5 equipped servers, such as Virtual SCSI, Virtual Ethernet SMT, Micro-Partitioning, and others. There are many other enhancements available with AIX 5L Version 5.3, and you can explore them in this book. For customers who are not familiar with the enhancements of AIX 5L through Version 5.2, a companion publication, AIX 5L Differences Guide Version 5.2 Edition, SG24-5765 is available. Five teenage girls with the power to see what boys are thinking about them must find dates for Skye Hamilton's "famous couples" costume party if they want to hang onto their popularity. A biographical dictionary of noteworthy men and women of the Central and Midwestern States. Shows how to construct a power supply, microprocessor, peripheral devices and a CRT terminal and explains the design considerations of each project. This book provides a wealth of detailed information that collectors, investors, and restorers of imported cars will not find in any other book. This massive volume spans the marques of imported vehicles. The list includes such familiar names as Alfa Romeo, Aston Martin, Bentley, Citroen, Jaguar, Lamborghini, Porsche, Rolls-Royce, Saab, and Volkswagon. Also in these pages, you'll find details on such lesser-known yet no less intriguing marques as Abarth, DAF, Frazer Nash, Humber, Iso, Nardi, Panhard, Peerless, Sabra and Skoda. The book also highlights model changes and corporate histories and provides value information on the most popular models of imported cars. Einstein's General Theory of Relativity leads to two remarkable predictions: first, that the ultimate destiny of many massive stars is to undergo gravitational collapse and to disappear from view, leaving behind a 'black hole' in space; and secondly, that there will exist singularities in space-time itself. These singularities are places where space-time begins or ends, and the presently known laws of physics break down. They will occur inside black holes, and in the past are what might be construed as the beginning of the universe. To show how these predictions arise, the authors discuss the General Theory of Relativity in the large. Starting with a precise formulation of the theory and an account of the necessary background of differential geometry, the significance of space-time curvature is discussed and the global properties of a number of exact solutions of Einstein's field equations are examined. The theory of the causal structure of a general space-time is developed, and is used to study black holes and to prove a number of theorems establishing the inevitability of singularities under certain conditions. A discussion of the Cauchy problem for General Relativity is also included in this 1973 book. The FAAT List is not designed to be an authoritative source, merely a handy reference. Inclusion recognizes terminology existence, not legitimacy. Entries known to be obsolete are included because they may still appear in extant publications and correspondence. With emphasis on the personal, business, and technology aspects that make using the Internet so unique, this handy reference presents more than 2,500 computer-related terms and industry-specific jargon for anyone who needs to learn the new language of the Net. Newbies as well as techies will find commonly used shorthand, modern office phrases, and a large collection of emoticons and ASCII art. An index sorts the terms into 10 popular categories with a complete list of international country codes and file extensions. The emphasis of this book is on practical design aspects for broadband A/D converters for communication systems. The embedded designs are employed for transceivers in the field of ADSL solutions and WLAN applications. An area- and power-efficient realization of a converter is mandatory to remain competitive in the market. The right choice for the converter topology and architecture needs to be done very carefully to result in a competitive FOM. The book begins with a brief overview of basic concepts about ADSL and WLAN to understand the ADC requirements. At architectural level, issues on different modulator topologies are discussed employing the provided technology node. The design issues are pointed out in detail for modern digital CMOS technologies, beginning with 180nm followed by 130nm and going down to 65nm feature size. Beside practical aspects, challenges to mixed-signal design level are addressed to optimize the converters in terms of consumed chip area, power consumption and design for high yield in volume production. Thus, careful considerations on circuit- and architectural- level are performed by introducing a dynamic-biasing technique, a feed-forward approach and a resolution in time instead of amplitude resolution. First published in 2002. Routledge is an imprint of Taylor & Francis, an informa company. By offering the new Service Routing Certification Program, Alcatel-Lucent is extending their reach and knowledge to networking professionals with a comprehensive demonstration of how to build smart, scalable networks. Serving as a course in a book from Alcatel-Lucent—the world leader in designing and developing scalable systems—this resource pinpoints the pitfalls to avoid when building scalable networks, examines the most successful techniques available for engineers who are building and operating IP networks, and provides overviews of the Internet, IP routing and the IP layer, and the practice of opening the shortest path first. This is the only book that completely lists accurate technical data for all cars imported into the U.S. market from 1946-2000. With many imports approaching the antique status, this book will be a big seller across all generations of car enthusiasts. From the grandiose European carriages of the late Forties to the hot, little Asian imports of the Nineties, every car to grace American roadways from across the Atlantic and Pacific is carefully referenced in this book. &#x26;Foreign car devotees will appreciate the attention given to capturing precise data on Appearance and Equipment, Vehicle I.D. Numbers, Specification Charts, Engine Data, Chassis, Technical Data, Options and Historical Information. &#x26;Collectors, restorers and car buffs will love this key book from noted automotive authors, James Flammang and Mike Covello. Analog Circuit Design contains the contribution of 18 tutorials of the 18th workshop on Advances in Analog Circuit Design. Each part discusses a specific to-date topic on new and valuable design ideas in the area of analog circuit design. Each part is presented by six experts in that field and state of the art information is shared and overviewed. This book is number 18 in this successful series of Analog Circuit Design, providing valuable information and excellent overviews of: Smart Data Converters: Chaired by Prof. Arthur van Roermund, Eindhoven University of Technology, Filters on Chip: Chaired by Herman Casier, AMI Semiconductor Fellow, Multimode Transmitters: Chaired by Prof. M. Steyaert, Catholic University Leuven, Analog Circuit Design is an essential reference source for analog circuit designers and researchers wishing to keep abreast with the latest development in the field. The tutorial coverage also makes it suitable for use in an advanced design. This Dictionary covers information and communication technology (ICT), including hardware and software; information networks, including the Internet and the World Wide Web; automatic control; and ICT-related computer-aided fields. The Dictionary also lists abbreviated names of relevant organizations, conferences, symposia and workshops. This reference is important for all practitioners and users in the areas mentioned above, and those who consult or write technical material. This Second Edition contains 10,000 new entries, for a total of 33,000.

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